



# **WA02B**

±5%, ±1%

# Thick film

General purpose chip resistors array

Size 0201x2 Flat Type

\*Contents in this sheet are subject to change without prior notice.





- 1. Small size and light weight
- 2. Reduced size of final equipment
- 3. Lower surface mounted assembly costs
- 4. Higher component and equipment reliability
- 5. Lead free / Halogen free

## **APPLICATION**

- Consumer electrical equipment, PDA, Digital Cam-coder, ...
- EDP, Computer application
- Mobile phone, Telecom
- Ram module

## DESCRIPTION

The resistors array is constructed in a high grade ceramic body (aluminum oxide). Internal metal electrodes are added at each end and connected by a resistive paste that is applied to the top surface of the substrate. The composition of the paste is adjusted to give the approximate resistance required and the value is trimmed to within tolerance by laser cutting of this resistive layer.

The resistive layer is covered with a protective coat. Finally, the two external end terminations are added. For ease of soldering the outer layer of these end terminations is a Tin (lead free) solder alloy.

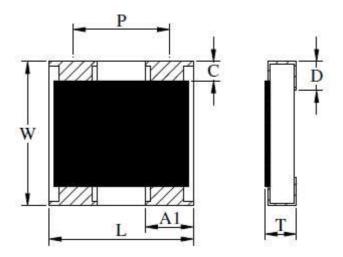


Fig 1. Outline of chip-R array WA02B



## QUICK REFERENCE DATA

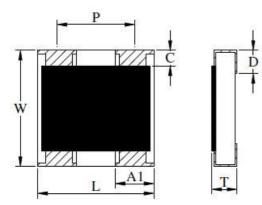
Item	General Specification		
Series No.	WA02B		
Size	0201x2 (0603x2)		
Termination construction	Flat type		
Resistance Tolerance	±5%, ±1% (E24 series)		
Resistance Range	±5%: 10 $\Omega$ ~ 1M $\Omega$ , Jumper (< 50m $\Omega$ )		
	±1%: 10Ω ~ 100KΩ		
TCR (ppm/°C)	10Ω ~ 29.5Ω: ≤ ± 350 ppm/°C		
	$30\Omega \sim 1M\Omega: \leq \pm 200 \text{ ppm/}^{\circ}\text{C}$		
Max. dissipation at T <sub>amb</sub> =70°C	1/32 W		
Max. Operation Voltage (DC or RMS)	12.5V		
Max. overload voltage	25V		
Rated current for Jumper	1A		
Operation temperature	-55 ~ +125'C		

Note :

- 1. This is the maximum voltage that may be continuously supplied to the resistor element, see "IEC publication 60115-8"
- 2. Max. Operation Voltage : So called RCWV (Rated Continuous Working Voltage) is determined by

 $RCWV = \sqrt{Rated Power \times Resistance Value}$  or Max. RCWV listed above, whichever is lower.

# **DIMENSIONS**(unit : mm)



	WA02B		
L	$0.80\pm0.10$		
w	$0.60\pm0.10$		
Т	$0.35\pm0.10$		
Р	0.50 typical		
A1	$0.30\pm0.10$		
С	$0.15\pm0.10$		
D	$0.15\pm0.10$		





No marking for WA02B chip resistors array

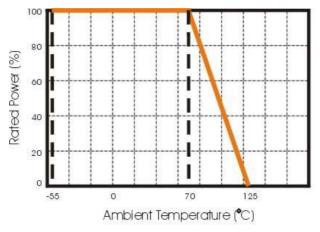
# FUNCTIONAL DESCRIPTION

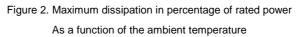
#### **Product characterization**

Standard values of nominal resistance are taken from the E24 series for resistors with a tolerance of  $\pm 5\%$ ,  $\pm 1\%$ . The values of the E24 series are in accordance with "IEC publication 60063"

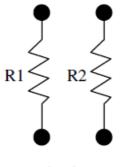
#### Derating

The power that the resistor can dissipate depends on the operating temperature; see Fig.2





## CONSTRUCTION



R1=R2

## MOUNTING

Due to their rectangular shapes and small tolerances, Surface Mountable Resistors are suitable for handling by automatic placement systems.

Chip placement can be on ceramic substrates and printed-circuit boards (PCBs).

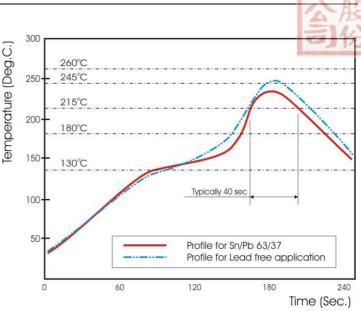
Electrical connection to the circuit is by individual soldering condition.

The end terminations guarantee a reliable contact.



The robust construction of chip resistors allows them to be completely immersed in a solder bath of 260°C for 10 seconds. Therefore, it is possible to mount Surface Mount Resistors on one side of a PCB and other discrete components on the reverse (mixed PCBs).

Surface Mount Resistors are tested for solderability at 235°C during 2 seconds. The test condition for no leaching is 260°C for 30 seconds. Typical examples of soldering processes that provide reliable joints without any damage are given in Fig 3.



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Fig 3. Infrared soldering profile for Chip Resistors array

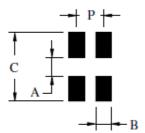
## CATALOGUE NUMBERS

The resistors have a catalogue number starting with .

WA02	В	472_	J	т	L
Size code	Type code	Resistance code	Tolerance	Packaging code	Termination code
WA02 : 0201	B : x2, Flat	5% E24 : 2 significant digits followed by no. of zeros and a blank 4.7Ω =4R7_ 10Ω =100_ 220Ω =221_ Jumper =000_ ("_" means a blank) 1%, E24+E96: 3 significant digits followed by no. of zeros 100Ω =1000 37.4KΩ =3742	J : ±5% F : ±1% P : Jumper	T : 7" 10kpcs Reel taping	L = Sn base (lead free)

Reeled tape packaging : 8mm width paper taping 7" reel 10,000pcs per reel,

## **Recommended Land Pattern Dimensions:**



Α	0.30		
В	0.30		
Р	0.50		
С	0.9		
	Unit : mm		





Essentially all tests are carried out according to the schedule of IEC publication 115-8, category LCT/UCT/56(rated temperature range : Lower Category Temperature, Upper Category Temperature; damp heat, long term, 56 days). The testing also meets the requirements specified by EIA, EIAJ and JIS.

The tests are carried out in accordance with IEC publication 68, "Recommended basic climatic and mechanical robustness testing procedure for electronic components" and under standard atmospheric conditions according to IEC 60068-1, subclause 5.3. Unless otherwise specified, the following value supplied :

Temperature: 15°C to 35°C.

Relative humidity: 45% to 75%.

Air pressure: 86kPa to 106 kPa (860 mbar to 1060 mbar).

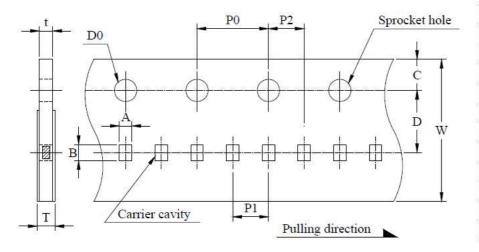
All soldering tests are performed with midly activated flux.

TFOT	PROCEDURE	REQUIREMENT		
TEST	PROCEDURE	Resistor	Jumper	
Electrical	- DC resistance values measurement	Within the specified		
Characteristics JISC5201-1: 1998	- Temperature Coefficient of Resistance (T.C.R) Natural resistance change per change in degree centigrade.			
Clause 4.8	$\frac{R_2 - R_1}{R_1(t_2 - t_1)} \times 10^6 \text{ (ppm/°C)}  t_1 : 20^\circ C + 5^\circ C - 1^\circ C$		N/a	
	R1 : Resistance at reference temperature			
	R <sub>2</sub> : Resistance at test temperature			
Short time overload (S.T.O.L) Clause 4.13	Permanent resistance change after a 5 seconds application of a voltage 2.5 times RCWV or the maximum overload voltage specified in the above	ΔR/R max. ±(2%+0.10Ω)	< 50mΩ	
Resistance to soldering heat(R.S.H) Clause 4.18	list, whichever is less. Un-mounted chips completely immersed for 10±1second in a SAC solder bath at 260°C±5°C	$\Delta$ R/R max. $\pm$ (1%+0.05 $\Omega$ ) no visible damage	< 50mΩ	
Solderability Clause 4.17	Un-mounted chips completely immersed for 2±0.5 second in a SAC solder bath at $235^{\circ}C \pm 5^{\circ}C$	good tinning (>95% covered) no visible damage		
Temperature cycling Clause 4.19	30 minutes at -55°C±3°C, 2~3 minutes at 20℃+5℃-1℃, 30 minutes at +125 °C±3°C, 2~3 minutes at 20℃+5℃-1℃, total 5 continuous cycles	$\Delta$ R/R max. $\pm$ (1%+0.05 $\Omega$ ) no visible damage	< 50mΩ	
Load life (endurance) Clause 4.25	1000 +48/-0 hours, loaded with RCWV or Vmax in chamber controller 70±2°C, 1.5 hours on and 0.5 hours off	$\Delta$ R/R max. $\pm$ (3%+0.1 $\Omega$ ) no visible damage	< 100mΩ	
Load life in Humidity Clause 4.24	1000 +48/-0 hours, loaded with RCWV or Vmax in humidity chamber controller at 40°C±2°C and 90~95% relative humidity, 1.5hours on and 0.5 hours off	$\Delta$ R/R max. $\pm$ (3%+0.1 $\Omega$ ) no visible damage	< 50mΩ	
Adhesion Clause 4.32	Pressurizing force: 5N, Test time: $10\pm1$ sec.	No remarkable damage or removal of the terminations.		
Insulation Resistance Clause 4.6	Apply the maximum overload voltage (DC) for 1 minute	R≧1GΩ		
Dielectric Withstand Voltage <b>Clause 4.7</b>	Apply the maximum overload voltage (AC) for 1 minute	No breakdown or flashover		



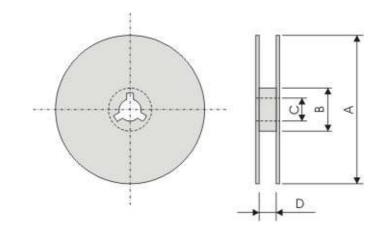


Paper Tape specifications (unit: mm)



Code	Dimensions (mm)
A	$0.70\pm0.10$
В	$0.90 \pm 0.10$
W	8.0 ± 0.3
С	$1.75 \pm 0.1$
D	$3.5 \pm 0.05$
<b>P</b> 0	$4.0 \pm 0.1$
P1	$2.0 \pm 0.05$
P2	$2.0 \pm 0.05$
Т	$0.5 \pm 0.1$
t	$0.43 \pm 0.05$
D0	$\phi 1.5^{+0.1}_{-0.0}$

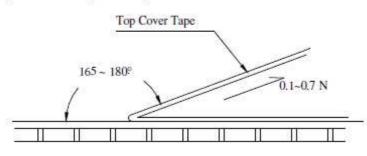
#### **Reel dimensions**



Symbol	А	В	С	D
(unit : mm)	$\Phi$ 178.0 ± 2.0	$\Phi 60.0 \pm 1.0$	13.0 ± 0.2	9.0 ± 0.3

#### Peeling force of top cover tape

The peel speed shall be about 300 mm/minute The peel force of top cover tape shall be between 0.1 to 0.7 N



#### Taping Qty

10,000pieces per 7" reel